

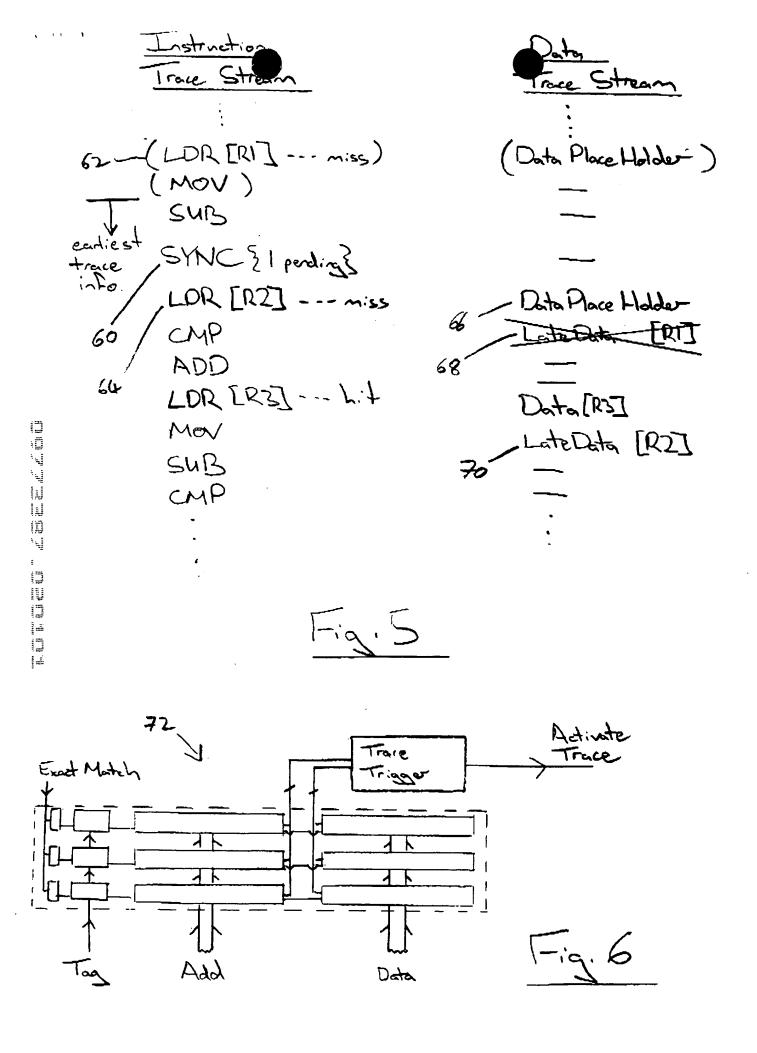
Inst H:+/Miss Data [Rm] H:+ NA CMP N/A LOR[R~] Mov Miss N/A CMP N/A Miss Miss ALM MOV 28 - Octo [R2] CMP AM N/A CICA

A corresponding to the control of th

Fig. 2

•				
Instructor	ii Data			
Trace Stream	Trace Stream			
20 _ LDR [RII - miss	32 - Data Place Holder Tag 1			
MOV	32 —			
CMP				
LDR[R] hit	Data [RZ]			
LDR [R3] miss	Data [RZ]  Data Place Holder Tag 2			
38 ADD	40 —			
MOV	42 - Late Data Tag 2 [R3]			
SUB	42 - Late Data Tag 2 [R3] 44 - Late Data Tag 1 [R1]			
	79			
	•			
Tia.	5			

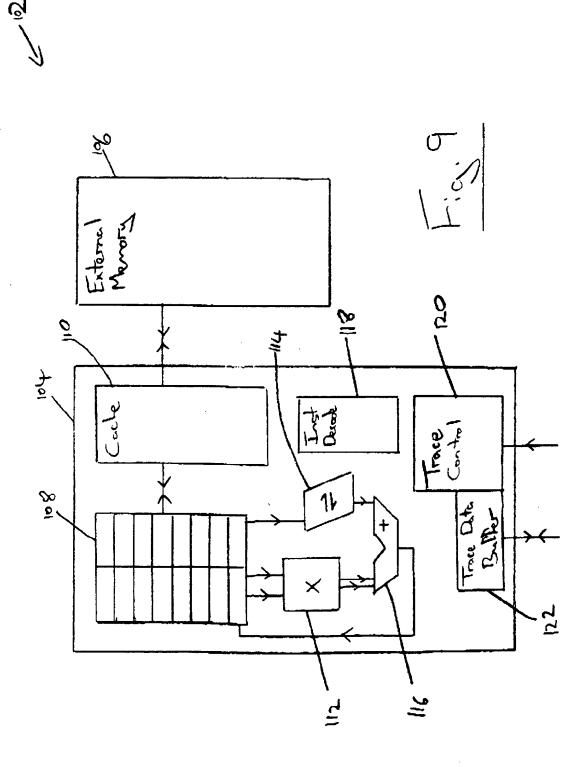
46 DDA LOR[R2] -- L:+ Octa [R2] 52 Data Place Holder 1 perding 54 Late Data [RI] LDR [R3] - · miss SUB SX \_ Data Place Hobber 1 perding Late Oata [R3] Late Data [Rb]



Instruction	Data Stream
``````````````````````````````````````	· ·
D-LOR [RI] miss	
D LDR [RI] miss ZH MOV	Data [RI] - 1
	76
Fig	.7

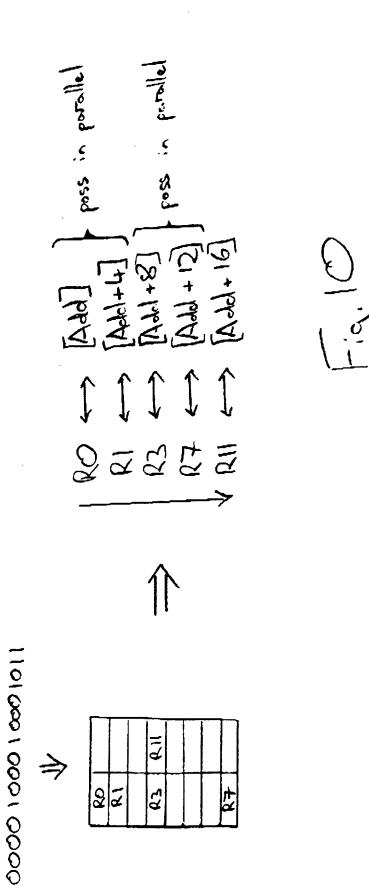
1=x=d Moteh	Add Match	Data Match	Trace Activation Point
	<b>√</b>	✓.	
	<b>✓</b>	×	none
×	<b>✓</b>		2
×	/	×	2

Fig. 8



SM R.[AM], Reglist

-16 bits



[SM] (DW] ; {Ad+8]; {PacHoldor}; {Pace Holdor}; True Octa Stream RII (2003) + 16] R7 (MA) +12] > R3 (Mad +2]! RI ( ASB +4) RO (M) [AN] العدو

Court back to R3

1-1. j

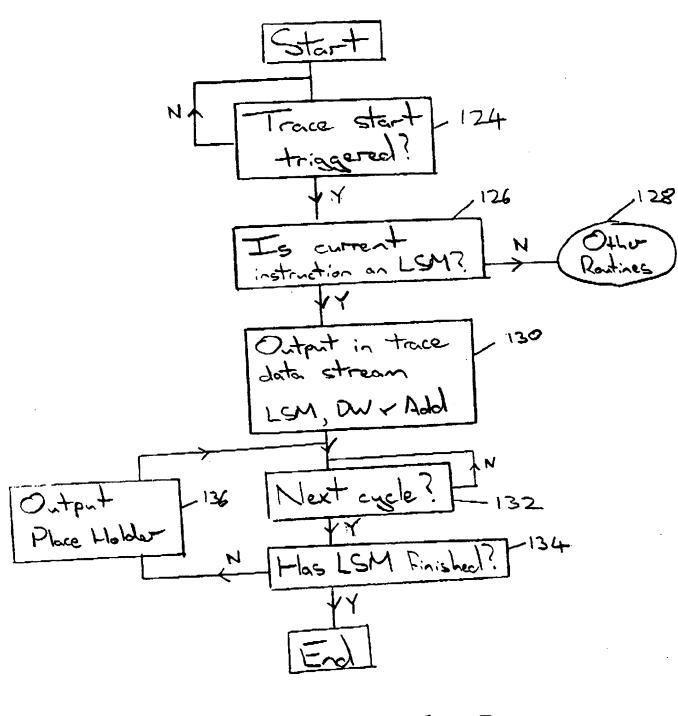


Fig. 12

of register transfers in and count back to Find register of first traced